

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Claim 1 (currently amended): A method of filtering a digital input signal having a digital value associated therewith, the method comprising:

determining a first rate of change of the digital input signal value relative to a previous digital output signal value;

supplying a rate adjusted digital output signal value that is (i) equivalent to the digital input signal ~~values~~ value when the first rate of change is less than a predetermined rate magnitude and (ii) equivalent to a predetermined rate limit value when the first rate of change equals or exceeds the predetermined rate magnitude;

determining a second rate of change of the rate adjusted digital output signal value relative to a previous rate adjusted digital output signal value; and

supplying an acceleration adjusted digital output signal value that is (i) equivalent to the rate adjusted digital output signal ~~values~~ value when the second rate of change is less than a predetermined acceleration magnitude and (ii) equivalent to the rate adjusted digital output signal ~~values~~ value with an acceleration adjustment value subtracted therefrom when the second rate of change equals or exceeds the predetermined acceleration magnitude.

Claim 2 (original): The method of Claim 1, wherein the step of determining the first rate of change comprises:

comparing the digital input signal value to the previous digital output signal value.

Claim 3 (original): The method of Claim 2, wherein the step of comparing comprises:

subtracting the previous digital output signal value from the digital input signal value to obtain a first difference value.

Claim 4 (original): The method of Claim 3, wherein the step of determining the first rate of change further comprises:

comparing the first difference value to the predetermined rate magnitude.

Claim 5 (original): The method of Claim 1, wherein the step of determining the second rate of change comprises:

comparing the rate adjusted digital output signal value to the previous rate adjusted digital output signal value.

Claim 6 (original): The method of Claim 5, wherein the step of comparing comprises:

subtracting the previous rate adjusted digital output signal value from the rate adjusted digital output signal value to obtain a second difference value.

Claim 7 (original): The method of Claim 6, wherein the step of determining the second rate of change further comprises:

comparing the second difference value to the predetermined acceleration magnitude.

Claim 8 (original): The method of Claim 6, wherein the acceleration adjustment value is set to a value that is equal to a magnitude difference between the second difference value and the predetermined acceleration magnitude.

Claim 9 (currently amended): A method of filtering a digital input signal having a digital value associated therewith, the method comprising:

determining a first rate of change of the digital input signal value relative to a previous digital output signal value;

supplying the digital input signal value as first stage digital output signal value when the first rate of change is less than a predetermined rate magnitude;

limiting the digital input signal value to a predetermined rate limit value and supplying the limited digital input signal value as the first stage digital output signal value when the first rate of change equals or exceeds [[a]] the predetermined rate magnitude;

determining a second rate of change of the first stage digital output signal value relative to a previous first stage digital output signal value;

supplying the first stage digital output signal value as a digital output signal value when the second rate of change is less than a predetermined acceleration magnitude; and

subtracting an acceleration adjustment value from the first stage digital output signal value to obtain an acceleration limited digital signal value and supplying the acceleration limited digital signal value as the digital output signal value when the second rate of change equals or exceeds the predetermined acceleration magnitude.

Claim 10 (original): The method of Claim 9, wherein the step of determining the first rate of change comprises:

comparing the digital input signal value to the previous digital output signal value.

Claim 11 (original) The method of Claim 10, wherein the step of comparing comprises:

subtracting the previous digital output signal value from the digital input signal value to obtain a first difference value.

Claim 12 (original): The method of Claim 11, wherein the step of determining the first rate of change further comprises:

comparing the first difference value to the predetermined rate magnitude.

Claim 13 (original): The method of Claim 9, wherein the step of determining the second rate of change comprises:

comparing the first stage digital output signal value to the previous first stage digital output signal value.

Claim 14 (original): The method of Claim 13, wherein the step of comparing comprises:

subtracting the previous first stage digital output signal value from the first stage digital output signal value to obtain a second difference value.

Claim 15 (original): The method of Claim 14, wherein the step of determining the second rate of change further comprises:

comparing the second difference value to the predetermined acceleration magnitude.

Claim 16 (original): The method of Claim 14, wherein the acceleration adjustment value is set to a value that is equal to a magnitude difference between the second difference value and the predetermined acceleration magnitude.

Claim 17 (currently amended): A method of filtering a digital input signal having a digital value associated therewith, the method comprising:

subtracting a previous digital output signal value from the digital input signal value to obtain a first rate of change of the digital input signal value relative to the previous digital output signal value;

comparing the first rate of change to a predetermined rate magnitude;

supplying a rate adjusted digital output signal value that is (i) equivalent to the digital input signal values value when the first rate of change is less than the predetermined rate magnitude and (ii) equivalent to a predetermined rate limit value when the first rate of change equals or exceeds the predetermined rate magnitude;

subtracting a previous rate adjusted digital output signal value from the rate adjusted digital output signal value to obtain a second rate of change of the rate adjusted digital output signal value relative to the previous rate adjusted digital output signal value;

comparing the second rate of change to a predetermined acceleration magnitude; and

supplying an acceleration adjusted digital output signal value that is (i) equivalent to the rate adjusted digital output signal values value when the second rate of change is less than the predetermined acceleration magnitude and (ii) equivalent to the rate adjusted digital output signal values value with an acceleration adjustment value subtracted therefrom when the second rate of change equals or exceeds the predetermined acceleration magnitude.

Claim 18 (currently amended): In a filter operable to receive a present digital input signal having a digital value associated therewith and to supply a filtered digital output signal having a digital value associated therewith, a method of filtering the present digital input signal, the method comprising:

receiving the present digital input signal value;

determining a first magnitude difference between the present digital input signal value and a previous filtered digital output signal value;

comparing the first magnitude difference to a predetermined rate limit value magnitude;

supplying the present digital input signal value as a present first stage digital output signal value when the first magnitude difference is less than the predetermined rate limit value magnitude;

limiting the present digital input signal value to [[the]] a predetermined rate limit value and supplying the limited digital input signal value as the present first stage digital output signal value when the first magnitude difference equals or exceeds the predetermined rate limit magnitude;

determining a second magnitude difference between the present first stage digital output signal value and a previous first stage digital output signal value;

comparing the second magnitude difference to a predetermined acceleration limit value magnitude;

supplying the present first stage digital output signal value as the digital filter output signal value when the second magnitude difference is less than the predetermined acceleration limit value magnitude; and

subtracting a residual acceleration value from the present first stage digital output signal value to obtain a present acceleration limited digital signal value and supplying the present acceleration limited digital signal value as the digital filter output signal value when the second magnitude difference equals or exceeds the predetermined acceleration limit value magnitude.

Claim 19 (original): A digital filter for filtering a digital input signal having a digital value associated therewith, comprising:

rate of change determining means for (i) determining a first rate of change of the digital input signal value relative to a previous digital output signal value and (ii) supplying a rate adjusted digital output signal value that is

- (a) equivalent to the digital input signal value when the first rate of change is less than a predetermined rate magnitude, and
- (b) equivalent to a predetermined rate limit value when the first rate of change equals or exceeds the predetermined rate magnitude;

and

acceleration determining means for (i) determining a second rate of change of the rate adjusted digital output signal value relative to a previous rate adjusted digital output signal value and (ii) supplying an acceleration adjusted digital output signal value that is

- (a) equivalent to the rate adjusted digital output signal value when the second rate of change is less than a predetermined acceleration magnitude, and
- (b) equivalent to the rate adjusted digital output signal value with a residual acceleration value subtracted therefrom when the second rate of change equals or exceeds the predetermined acceleration magnitude.

Claim 20 (currently amended): The filter of Claim 19, wherein the rate of change determining means comprises:

first comparison means for comparing the digital input signal value to the previous digital output signal value.

Claim 21 (original): The filter of Claim 20, wherein the first comparison means comprises:

first subtraction means for subtracting the previous digital output signal value from the digital input signal value to obtain a first difference value.

Claim 22 (original): The filter of Claim 21, wherein the rate of change determining means further comprises:

second comparison means for comparing the first difference value to the predetermined rate magnitude.

Claim 23 (original): The filter of Claim 19, wherein the acceleration determining means comprises:

third comparison means for comparing the rate adjusted digital output signal value to the previous rate adjusted digital output signal value.

Claim 24 (original): The filter of Claim 23, wherein the third comparison means comprises:

second subtraction means for subtracting the previous rate adjusted digital output signal value from the rate adjusted digital output signal value to obtain a second difference value.

Claim 25 (original): The filter of Claim 24, wherein the acceleration determining means further comprises:

fourth comparison means for comparing the second difference value to the predetermined acceleration magnitude.

Claim 26 (original) : The filter of Claim 24, wherein the acceleration determining means further comprises:

third subtraction means for subtracting the second difference value from the predetermined acceleration magnitude to obtain the residual acceleration value.

Claim 27 (currently amended): A digital filter for filtering a digital input signal having a digital value associated therewith, comprising:

first subtraction means for subtracting [[the]] a previous digital output signal value from the digital input signal value to obtain a first rate of change of the digital input signal value relative to [[a]] the previous digital output signal value;

first comparison means for (i) comparing the first rate of change value to a predetermined rate magnitude and (ii) supplying a rate adjusted digital output signal value that is

(a) equivalent to the digital input signal value when the first rate of change is less than the predetermined rate magnitude, and

(b) equivalent to a predetermined rate limit value when the first rate of change equals or exceeds the predetermined rate magnitude;

second subtraction means for subtracting a previous rate adjusted digital output signal value from the rate adjusted digital output signal value to obtain a second rate of change of the rate adjusted digital output signal value relative to [[the]] a previous rate adjusted digital output signal value[[.]];.

~~fourth~~ second comparison means for (i) comparing the second rate of change to a predetermined acceleration magnitude and (ii) supplying an acceleration adjusted digital output signal value that is

(a) equivalent to the rate adjusted digital output signal value when the second rate of change is less than the predetermined acceleration magnitude, and

(b) equivalent to the rate adjusted digital output signal value with a residual acceleration value subtracted therefrom when the second rate of change equals or exceeds the predetermined acceleration magnitude.

Claim 28 (currently amended): A computer readable storage medium containing computer executable code for instructing a processor to filter a digital input signal having a digital value associated therewith by performing the steps of:

determining a first rate of change of the digital input signal value relative to a previous digital output signal value;

supplying a rate adjusted digital output signal value that is (i) equivalent to the digital input signal values value when the first rate of change is less than a predetermined rate magnitude and (ii) equivalent to a predetermined rate limit value when the first rate of change equals or exceeds the predetermined rate magnitude;

determining a second rate of change of the rate adjusted digital output signal value relative to a previous rate adjusted digital output signal value; and

supplying an acceleration adjusted digital output signal value that is (i) equivalent to the rate adjusted digital output signal values value when the second rate of change is less than a predetermined acceleration magnitude and (ii) equivalent to the rate adjusted digital output signal values value with an acceleration adjustment value subtracted therefrom when the second rate of change equals or exceeds the predetermined acceleration magnitude.